AMENDMENTS TO THE ABSTRACT

Please amend the abstract as follows.

A low-complexity, high accuracy model of a CPU anti-resonance system has been developed. The model includes a <u>simulated</u> load model—that <u>simulates the performance of the anti-resonance circuit</u>, a <u>simulated</u> transistor that <u>simulates models</u>—the performance of a high frequency capacitor, and a <u>simulated</u> capacitor that <u>simulates models</u>—the performance of <u>an the-intrinsic capacitance</u> of a section of the microprocessor. All of the elements of the model

are connected in parallel.

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